

METHOD AND APPARATUS FOR PROCESSING SUBSTRATES  
AND METHOD FOR MANUFACTURING A SEMICONDUCTOR DEVICE

Field of the Invention

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The present invention relates to a method and an apparatus for processing substrates and a method for manufacturing semiconductor devices; and, more particularly, to a substrate processing apparatus adequate for processing a small number of substrates at a time at reduced costs and methods for processing substrates and manufacturing semiconductor devices by using the substrate processing apparatus.

15 Background of the Invention

In manufacturing integrated circuit (IC) devices, a batch type vertical diffusion and chemical vapor diffusion (CVD) system (hereinafter, "a batch-type CVD system") is widely used to diffuse impurities onto wafers or to form on the wafers CVD films such as insulating films, metal films or the like.

In substrate processing apparatuses including the batch-type CVD system, a plurality of wafers are transferred while being contained in a carrier. Two kinds of carriers have been conventionally used. One is a box-shaped cassette

having a pair of openings on two opposite sides thereof and the other is a box-shaped FOUP (front opening unified pod; hereinafter, "pod") having an opening on one side thereof with a pod door removably mounted thereon. When the pod is  
5 used as a carrier for the wafers, the wafers can be kept protected from contaminations of ambient atmosphere while being transferred since the pod containing the wafers is airtightly closed. Accordingly, the requirement of cleanliness for a clean room in which the substrate  
10 processing apparatus is installed can be relaxed, thereby reducing the cost for maintenance of the clean room. For such reasons, the pod is gaining popularity as a wafer carrier in the batch-type CVD system recently.

In general, a batch-type CVD system having a pod as a  
15 wafer carrier includes a process tube for performing a desired process to wafers, a boat for loading/unloading a plurality, e.g. 150 sheets, of wafers into/from the process tube, a wafer loading port for supporting one or more pods while the wafers are transferred between the pods and the  
20 boat, a pod stage for placing thereon pods unloaded from and to be introduced into the CVD system, pod shelves for temporarily storing pods and a pod transfer device for conducting pod transfer between the pod stage and the pod shelves and between the pod shelves and the wafer loading  
25 port.

In such a batch-type CVD system described above, the

5 pods are carried to the pod stage and then transferred by  
the pod transfer device to the pod shelves to be temporarily  
kept thereon. Then, a plurality of pods kept on the pod  
shelves are transferred to the wafer loading port through  
10 repetitive operations of the pod transfer device.  
Thereafter, the wafers contained in the pods are charged  
into the boat by the wafer transfer device and then the boat  
is loaded into the process tube in which the wafers are  
processed. The processed wafers are unloaded from the  
10 process tube and discharged from the boat to be carried into  
the empty pods waiting on the wafer loading port through the  
repetitive operations of the wafer transfer device. The  
pods containing the processed wafers therein are repeatedly  
transferred to the pod stage by the pod transfer device  
15 after being temporarily kept on the pod shelves.

Recently, in manufacturing large scale integration  
(LSI) devices or the like, a quick turned around time (QTAT)  
type production, in which the required time from input of  
wafers to finish is as shortened as possible, is gaining  
20 importance. The QTAT manufacturing method has increased a  
demand for a small-batch type CVD system suitable for  
processing 25 sheets or less of wafers during one batch  
process, i.e., for a small batch process. Such a small-  
batch type CVD system requires only two or three pods for  
25 one batch process.

However, the above described conventional batch-type

CVD system is suitable for a large batch dealing with a large number, e.g., 150 sheets, of substrates during one batch process. Accordingly, the footprint required for the pod shelves or the number of layers of each pod shelf should be increased or the shelves should be built to be rotatable so as to accommodate many pods thereon. Thus, the use of such a conventional batch-type CVD system for a small batch process would end up with the unnecessarily large and complicated system. Further, initial costs or running costs of the system would also be unnecessarily increased. Still further, the conventional batch-type CVD system cannot fully meet requirements of the QTAT type production.

#### Summary of the Invention

It is, therefore, an object of the present invention to provide a substrate processing apparatus suitable for batch-processing a small number of substrates at reduced costs and methods for processing the substrates and manufacturing semiconductor devices by using the substrate processing apparatus.

In accordance with an aspect of the present invention, there is provided a substrate processing apparatus, comprising: a housing, a process tube being located in the housing for performing variable batch processes on substrates, the substrates including product substrates

which are used for manufacturing final products, and product substrate carriers for transferring the product substrates, the product substrate carriers having a capacity of a predetermined number of substrates, wherein a number of the product substrates processed during one batch process are set to be less than or equal to the predetermined number of the substrates and all of the product substrates contained in one product substrate carrier is processed in the process tube at a same time.

10 In accordance with a second aspect of the present invention, there is provided a method for performing a predetermined batch process to substrates, the substrates including product substrates which are used for manufacturing final products, the method comprising the steps of: transferring the product substrates by using product substrate carriers, the product substrate carriers having a capacity of a predetermined number of substrates, and performing the predetermined batch process on the product substrates, wherein a number of the product substrates processed during the predetermined batch process are set to be less than or equal to the predetermined number of the substrates and all of the product substrates contained in one product substrate carrier is processed in the predetermined batch process.

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### Brief Description of the Drawings

The above and other objects and features of the present invention will become apparent from the following  
5 description of preferred embodiments given in conjunction with the accompanying drawings, in which:

Fig. 1 is a perspective view of a small-batch type CVD system in accordance with a first embodiment of the present invention;

10 Fig. 2 provides a plan view of the small-batch type CVD system shown in Fig. 1;

Fig. 3 depicts a cross-sectional view of the small-batch type CVD system shown in Fig. 1;

15 Fig. 4 sets forth a perspective view of a pod transfer device;

Figs. 5A to 5D illustrate front views for explaining pod transferring processes performed by the pod transfer device in accordance with the first embodiment of the present invention;

20 Fig. 6A to 6E offer front views for illustrating wafer charging and discharging processes in accordance with the first embodiment of the present invention;

Fig. 7 shows a perspective view of a small-batch type CVD system in accordance with a second embodiment of the  
25 present invention;

Fig. 8 presents a plan view of the small-batch type

CVD system shown in Fig. 7; and

Fig. 9 represents a cross-sectional view of the small-batch type CVD system shown in Fig. 7.

5     Detailed Description of the Preferred embodiments

First, a substrate processing apparatus in accordance with a first embodiment of the present invention will be described hereinafter.

10         The substrate processing apparatus of the first preferred embodiment is a small-batch type vertical diffusion and CVD system (hereinafter, "a small-batch type CVD system") which processes a small number, e.g., about 50 sheets or less, of substrates during one batch process. The  
15     small-batch type CVD system processes or treats product wafers serving as substrates for producing actual device products thereon and uses pods as carriers therefor. In the following descriptions, a front, a rear, a left and a right side are defined with reference to Fig. 2, wherein the front  
20     side refers to where a pod opener 21 is located; the left side refers to where a clean unit 17 is installed; and the rear side and the right side respectively refer to an area opposite to the front side and the left side.

As shown in Figs. 1 to 3, the small-batch type CVD  
25     system 1 includes a housing 2, a heater unit 3 and a process tube 4. The heater unit 3 is vertically installed at an

upper portion of the rear side of the housing 2 and the process tube 4 is concentrically disposed within the heater unit 3. The process tube 4 has a gas supply pipe 5 for supplying a process gas or a purge gas into the process tube 4, and an exhaust pipe 6 for evacuating the process tube 4.

Disposed at a lower portion of the rear side of the housing 2 is an elevator 7 with feed screw elements or the like. The elevator 7 allows a sealing cap 8 horizontally installed below the process tube 4 to move up and down. The sealing cap 8 airtightly seals a throat of the process tube 4, i.e., an opening at a lower part of the process tube 4, and vertically supports a boat 9. The boat 9 accommodates a plurality, e.g., less than or equal to about 50 sheets, of wafers (W) and arranges the wafers (W) in such a manner that the centers of the wafers are vertically aligned while keeping the wafers parallel to each other. The wafers are loaded into or unloaded from the process tube 4 as the boat is moved up or down by the elevator 7.

Referring to Figs. 2 to 3, at the front region of the housing 2 is installed a wafer transfer device 10 for charging and discharging the wafers (W) into and from the boat 9. The wafer transfer device 10 has a rotary actuator 11, which circularly moves a first linear actuator 12 in a horizontal plane. The first linear actuator 12 is installed on a top surface of the rotary actuator 11. Placed on a top surface of the first linear actuator 12 is a second linear



actuator 13, which is translationally moved in a horizontal plane by the first linear actuator 12. On a top surface of the second linear actuator 13 is installed a moving stage 14, which is horizontally and translationally moved by the  
5 second linear actuator 13. The moving stage 14 has plural sets of tweezers 15 (5 sets of tweezers in this preferred embodiment) vertically disposed at regular intervals, the tweezers in each set horizontally supporting a wafer at the bottom thereof. The wafer transfer device 10 is moved up  
10 and down by an elevator 16 including a feed screw mechanism.

As illustrated in Fig. 2, on a left wall of the rear side of the housing 2 is disposed a clean unit 17 for spraying clean air to the boat 9. Further, at a left side of the center portion of the housing 2 is provided a wafer  
15 stocker 18 having a similar configuration as the boat 9. The wafer stocker 18 stores a plurality of side dummy wafers thereon.

Referring to Figs. 1 to 3, a wafer loading/unloading opening 20 through which the wafers (W) are transferred into  
20 and from the housing 2 is prepared on a central portion of a front wall thereof. At the wafer loading/unloading opening 20 is installed a pod opener 21. The pod opener 21 has a pod loading port 22 for mounting a pod 26 and a pod door removing/restoring device 23 for opening and closing an  
25 opening of the pod 26 mounted on the pod loading port 22 by removing and restoring a door 27 of the pod 26.

As illustrated in Figs. 1 to 3, a first and a second pod stage 24 and 25 are respectively installed at a left side and a right side of the pod loading port 22 so as to receive the pod 26 thereon. The pods 26 are supplied to or removed from the first and the second pod stage 24 and 25 by a pod conveying system (not shown) such as a rail guided vehicle (RGV) equipped with a pod transfer mechanism, an automatic guided vehicle (AGV) equipped with a pod transfer mechanism and a personnel guided vehicle (PGV) equipped with a manual pod transfer mechanism, and the like. At an upper portion of the front region of the housing 2, there is installed a pod carrying device 30 (illustrated in detail in Fig. 4) for transferring the pod 26 between the pod opener 21 and either one of the first and the second pod stage 24 and 25.

Referring to Fig. 4, there is provided a perspective view of the pod carrying device 30. The pod carrying device 30 has an installation plate 31 vertically disposed and elongated in a left-right direction. At an upper portion of the front surface of the installation plate 31 is installed an angle shaped base plate 33, which is horizontally arranged in the left-right direction and fixed by a bracket 32. A pair of brackets 34 are installed at a left and a right end of the upper surface of the base plate 33, and a pulley 35 is rotatably installed at each of the brackets 34. A timing belt 36 is movably wound between the two pulleys 35.

Halfway between the two brackets 34 is fixed a central bracket 37 to which a driving pulley 39 rotated by a servomotor 38 is connected. The driving pulley 39 is disposed between an upper travel portion and a lower travel portion of the timing belt 36 and a lower surface of the upper travel portion of the timing belt 36 is wound around the driving pulley 39 such that the timing belt 36 is moved when the driving pulley 39 rotates. The central bracket 37 also has two tension pulleys 40 rotatably installed at a left and a right side of the driving pulley 39. The tension pulleys 40 are in pressurized contact with an upper surface of the upper travel portion of the timing belt 36, so that the timing belt 36 is pushed toward the driving pulley 39 with an appropriate tension applied thereto.

A linear guide rail 41 is horizontally installed in the left-right direction on a vertical member of the angle shaped base plate 33 and a conveying block 42 is installed on the guide rail 41 in such a manner as to be able to move in the left-right direction. On a top surface of the conveying block 42 is prepared a connecting piece 43, which is coupled to a central part of the lower travel portion of the timing belt 36. Accordingly, the conveying block 42 is movable in the left-right direction on the guide rail 41 when the timing belt 36 travels.

A first air cylinder 45 having a piston rods 46 is installed at the conveying block via a bracket 44 in a

vertical downward direction. A second air cylinder 48 is connected to the piston rods 46 via a bracket 47 and is horizontally disposed in the left-right direction. The second air cylinder 48 has two piston rods 49 respectively installed at two opposite ends thereof, wherein the piston rods 49 retract and extend in the left-right direction. At the distal ends of the piston rods 49 at each end of the second air cylinder 48, there is installed a lower clamping element 50. The two lower clamping elements 50 horizontally approach to spaces right below the handle of the pod 28 to face a bottom surface of the handle 28 when the piston rods 49 retract.

Further, a third air cylinder 51 having piston rod 52 is installed at a central part of a lower surface of the second air cylinder 48 in a vertical downward direction. At the end of the piston rod 52 is installed an upper clamping element 53, which is brought into contact with a top surface of the handle 28 of the pod 26 when the piston rod 52 extends and clamps the handle 28 of the pod 26 together with the lower clamping elements 50. The pod carrying device 30 configured as described above is covered by a case 54, as shown in Fig. 1.

Referring to Figs. 5A to 6E, a pod loading/unloading process and a wafer charging/discharging process in accordance with a first embodiment of the present invention will be described hereinafter, wherein the small-batch type

CVD system configured as described above is used in forming films on wafers.

5 A pod 26A provided on the first pod stage 24 (hereinafter, "a first pod") contains 25 sheets of side dummy wafers representing substrates not to be used to produce actual devices and a pod 26B supplied to the second pod stage 25 (hereinafter, "a second pod") contains 25 sheets of product wafers serving as substrates for producing actual devices.

10 The first and the second pod 26A and 26B are respectively provided onto the first and the second pod stage 24 and 25 by using the pod conveying system. Thereafter, the first pod 26A conveyed to the first pod stage 24 is clamped and picked up by the pod carrying device  
15 30 as shown in Figs. 5A to 5D and transferred to the loading port 22 of the pod opener 21 as illustrated in Figs. 6A to 6B.

20 Pod pick-up and put-down process of the pod carrying device 30 will now be described hereinafter with reference to Figs. 5A to 5D.

The pod carrying device 30 picks up the pod 26 mounted on the first pod stage 24 through a series of operations as follows. First, as shown in Fig. 5A, the upper clamping element 53 is moved to right above the handle 28 of the pod  
25 26 mounted on the first pod stage 24. To be more specific, the driving pulley 39 operated by the servomotor 38 makes

the timing belt 36 travel and the timing belt 36 causes in turn the conveying block 42 to move in the left-right direction along the guide rail 41 to locate the upper clamping element 53 to a desired position. Meanwhile, the  
5 lower clamping elements 50 prepared are maintained by the piston rods 49 of the second air cylinder 48 to be disposed above the handle 28 of the pod 26 in open state.

Next, as illustrated in Fig. 5B, the upper clamping element 53 and the lower clamping elements 50 in open state  
10 are lowered by extending the piston rods 46 of the first air cylinder 45 until the upper clamping element 53 reaches to a position right above the handle 28 and the lower clamping elements 50 approach to positions below the handle 28. While the upper clamping element 53 and the lower clamping  
15 elements 50 are lowered, the vertical distance therebetween is maintained intact.

Thereafter, as shown in Fig. 5C, the left and right lower clamping elements 50 are moved into positions right below the handle 28 by retracting the piston rods 49 of the  
20 second air cylinder 48. Accordingly, upper surfaces of the end portions of the left and right lower clamping elements 50 become to face a left and a right end portion of a lower surface of the handle 28, respectively, maintaining a slight distance therebetween.

25 Subsequently, as shown in Fig. 5D, since the end portions of the left and the right lower clamping elements

50 are brought in contact with the left and the right end portion of the lower surface of the handle 28 when the piston rods 46 of the first air cylinder 45 retracts, the pod 26 is picked up by the lower clamping elements 50. 5 Concurrently, the upper clamping element 53 is lowered by extending the piston rod 52 of the third air cylinder 51, so that the handle 28 becomes clamped by the upper clamping element 53 and the lower clamping elements 50. Since the handle 28 of the pod 26 is clamped by the upper clamping element 53 and the lower clamping elements 50 while being 10 transferred, the pod 26 can be moved safely and promptly by the pod carrying device 30.

The pod carrying device 30 transfers the pod 26 whose handle 28 is clamped by the upper clamping element 53 and 15 the lower clamping elements 50 from the first pod stage 24 to a position above the loading port 22 of the pod opener 21. More specifically, the driving pulley 39 operated by the servomotor 38 makes the timing belt 36 travel and the timing belt 36 allows in turn the conveying block 42 to travel in 20 the left-right direction along the guide rail 41. Accordingly, the pod 26 can be successfully transferred to the position right above the loading port 22.

Then, the pod 26 is put down on the loading port 22 by the pod carrying device 30 operating as follows in a reverse 25 sequence of the processes described above. Referring to Fig. 5C, the upper clamping element 53 is slightly lifted by

retracting the piston rod 52 of the third air cylinder 51. Then, the left and right lower clamping elements 50 are lowered by extending the piston rods 46 of the first air cylinder 45. As a result, the pod 26 is moved to be mounted  
5 on the loading port 22. Referring to Fig. 5B, the left and right lower clamping elements 50 are opened by extending the piston rods 49 of the second air cylinder 48 and moved away from the handle 28. Thereafter, as shown in Fig. 5A, the upper clamping element 53 and the lower clamping elements 50  
10 in open state are lifted by retracting the piston rods 46 of the first air cylinder 45 and returned to a waiting position, wherein the vertical distance between the upper clamping element 53 and the lower clamping elements 50 is remains unchanged while being lifted.

15 The door 27 of the first pod 26A provided onto the loading port 22 by the above operations of the pod carrying device 30 is separated by the pod door removing/restoring device 23 so that the opening of the first pod 26A is opened and the wafer contained therein can be taken out.

20 Then, the plurality of side dummy wafers (Wa) accommodated in the first pod 26A are sequentially carried out of the first pod and charged into the boat 9 by the wafer transfer device 10. At this time, the side dummy wafers (Wa) are properly distributed in an upper end part  
25 and a lower end part of the boat 9 as can be seen in Fig. 3.

When the side dummy wafers (Wa) in the first pod 26A



are completely loaded into the boat 9, the pod door 27 once separated before is remounted to the empty first pod 26A by the pod door removing/restoring device 23. Then, the first pod 26A is returned to the first pod stage 24 from the loading port 22 by the pod carrying device 30, as shown in Figs. 6B and 6C.

Next, as illustrated in Figs. 6C and 6D, the second pod 26B is transferred to the loading port 22 from the second pod stage 25 by the pod carrying device 30 which is operated in the same way as described above. Then, the second pod 26B is mounted on the loading port 22 and the door 27 of the second pod 26B is separated by the pod door removing/restoring device 23, so that the wafers contained in the pod 26B can be taken out.

Thereafter, product wafers (Wb) contained in the second pod 26B are loaded onto the boat 9 in sequence by the wafer transfer device 10, as can be seen from Figs. 6D and 6E. Herein, the product wafers (Wb) are loaded into the boat 9 in such a manner that the product wafers (Wa) are distributed between two groups of the side dummy wafers (Wa) arranged at the upper end part and the lower end part of the boat 9, as shown in Fig. 3.

Upon the completion of the product wafer (Wb) loading process into the boat 9, the empty second pod 26 may remain on the loading port 22. At this time, the door 27 may be remounted on the second pod 26A by the pod door

removing/restoring device 23.

When the predetermined total number of, e.g. 30 to 32, side dummy wafers (Wa) and product wafers (Wb) are completely loaded into the boat 9, the boat 9 is lifted by the boat elevator 7 and introduced into a process room of the process tube 4. If the boat 9 reaches an upper limit point, a periphery of an upper surface of the sealing cap 8, which supports the boat 9, airtightly closes the process tube 4, so that the process room is hermetically sealed.

Then, the process room of the process tube 4 is evacuated through the exhaust pipe 6 to reduce the pressure therein down to a predetermined level. Thereafter, the process room is heated by the heater unit 3 to reach to a predetermined temperature. Next, a predetermined flow rate of process gas is supplied into the process room through the gas supply pipe 5. Through such operations, a desired film corresponding to preset process conditions is formed on the product wafers (Wb).

In the first preferred embodiment of the present invention, the number of the product wafers that can be processed during one batch process is set to be equal to or less than the maximum number of product wafers that can be contained in one product substrate carrier, i.e., the second pod 26B. Further, all of the product wafers contained in the one product substrate carrier, i.e., the second pod 26B, is set to be processed in the process room of the process

tube 4 at the same time. That is, the maximum number of product wafers that can be processed in one batch is preferably set to be the same as that which can be accommodated in the second pod 26B. However, if the maximum  
5 number of product wafers that can be processed by one batch in the CVD system 1 is set to be, e.g., 20, which is less than 25, the maximum number of product wafers that can be accommodated in the second pod 26B, the number of product wafers carried by the second pod 26B is controlled to be not  
10 greater than 20.

After a predetermined processing time has elapsed, the boat 9 is moved downward by the boat elevator 7 and returned to a waiting position, i.e., a wafer charging/discharging station, with the processed product wafers (Wb) and the side  
15 dummy wafers (Wa) kept therein.

When the boat 9 is returned to the waiting position, the processed product wafers (Wb) are first discharged from the boat 9 by the wafer transfer device 10 and carried into the empty second pod 26B that has been waiting on the  
20 loading port 22 as shown in Fig. 6E. The processed product wafers (Wb) are preferably unloaded prior to the side dummy wafers (Wa) in order to prevent the processed product wafers (Wb) from being contaminated by the particulates which can be generated during the unloading process of the side dummy  
25 wafers (Wa).

After all the processed product wafers (Wb) are

carried into the empty second pod 26B, the second pod 26B filled with the processed product wafers (Wb) are closed by the pod door 27 which is remounted thereon by the pod door removing/restoring device 23. Then, as can be seen from  
5 Figs. 6D and 6C, the second pod 26B is transferred from the loading port 22 to the second pod stage 25 through the operations of the pod carrying device 30 described above.

Next, as shown in Fig. 6C and 6B, the empty first pod 26A that has been waiting on the first pod stage 24 is  
10 transferred from the first pod stage 24 to the loading port 22 by the pod carrying device 30 which operates as described above. Subsequently, the door 27 of the first pod 26A is opened by the pod door removing/restoring device 23, and the used side dummy wafers (Wa) are carried from the boat 9 to  
15 the empty first pod 26A by the wafer transfer device 10.

Next, the first pod 26A filled with the used side dummy wafers (Wa) are closed by the pod door removing/restoring device 23 and transferred from the loading port 22 to the first pod stage 24 by the pod  
20 carrying device 30. Alternatively, the first pod 26A can remain on the loading port 22 waiting for a next charging process without being returned to the first pod stage 24.

Meanwhile, the second pod 26B returned to the second pod stage 25 along with the processed product wafers (Wb)  
25 contained therein is then transferred by the conveying system to a next process line for cleaning, a film test, or

the like. The processed product wafers (Wb) having films formed thereon are transferred to a lithography process line or an etching process line to be subjected to required treatments. As a result, IC devices are finally produced in accordance with the first embodiment of the present invention. Meanwhile, the first pod 26A returned to the first pod stage 24 while containing therein the used side dummy wafers (Wa) is kept thereon to be repeatedly used for next batch processes as long as the side dummy wafers (Wa) can be reused. However, when degrees of bending or degrees of contamination of the side dummy wafers (Wa) become to exceed a tolerance limit after repeated uses, the first pod 26A containing therein the side dummy wafers (Wa) is transferred by the conveying system from the first pod stage 24 to, e.g., a side dummy wafer exchanging process line, which is one of the process lines for manufacturing the IC devices in accordance with the present invention.

Afterward, the above-described wafer processing method in accordance with the first embodiment are repeated to perform a batch process on everyone of, e.g., 25 product wafers by using the small-batch type CVD system. As a result, IC devices can be produced in accordance with the first embodiment of the present invention.

Following effects can be provided in accordance with the first embodiment of the present invention as described above.

1) By setting the number of product wafers to be less than 25 sheets for one batch process, the small-batch type CVD system only needs to deal with just two pods, a first pod carrying product wafers and a second pod carrying side dummy wafers. Accordingly, a tact time can be considerably reduced.

2) By using a small number of pods, the small-batch type CVD system can be minimized. Thus, initial costs or running costs of the CVD system can be greatly diminished and a footprint can also be reduced to thereby efficiently utilize the clean room.

3) By reducing the number of side dummy wafers needed for one batch process, initial costs or running costs involved with the side dummy wafers can be largely curtailed.

4) A first and a second pod stage are respectively installed at a left and a right side of the loading port, and the pods are transferred between the loading port and the pod stages by the pod transfer device. This system eliminates the need for shelves for temporarily holding the pods, thereby further diminishing initial costs or running costs of the substrate processing apparatus.

5) By installing the pod transfer device right above the pod opener and the pod stages, the efficiency of the small-batch type CVD system can be improved without increasing the floor area.

6) The pod transfer device clamps the pods by using a

clamping device having an upper clamping element and a lower clamping element while carrying the pods. Accordingly, the pods can be safely delivered without being dropped on the way and thus the wafers contained in the pods can also be  
5 safely carried without any damage caused.

7) By installing the loading port and the pod stages at the outside of the housing, it is prevented for any contaminants to enter the inside of the housing and the cleanliness of the housing can be effectively kept. As a  
10 result, quality and reliability of the wafer processing method and IC manufacturing method can be improved.

Fig. 7 is a perspective view of a small-batch type CVD system in accordance with a second embodiment of the present invention. Referring to Fig. 8 and Fig. 9, there  
15 are respectively illustrated a plan view and a cross-sectional view of the CVD system shown in Fig. 7.

The small-batch type CVD system 1A in accordance with the second preferred embodiment is identical to the small-batch type CVD system 1 of the first embodiment excepting  
20 that: the pod carrying device 30 is not provided; instead, a first wafer loading/unloading opening 20A and a second wafer loading/unloading opening 20B are provided on a front wall of a housing 2, respectively facing a first pod stage 24 and a second pod stage 25; and a first pod opener 21A and a  
25 second pod opener 21B are respectively installed on the first and the second wafer loading/unloading opening 20A and

20B.

Hereinafter, with the explanation of the operation of the small-batch type CVD system 1A, a film-formation process in accordance with the second embodiment of the present invention will be described, particularly focusing on characteristic features thereof, i.e., a pod loading/unloading process and a wafer charging/discharging process. A pod provided on the first pod stage 24 (hereinafter, "a first pod 26A") contains 25 sheets of side dummy wafers representing substrates not to be used to produce actual devices and a pod provided on the second pod stage 25 (hereinafter, "a second pod 26B") contains 25 product wafers serving as substrates for producing actual devices. The first and the second pod 26A and 26B are provided onto the first and the second pod stage 24 and 25, respectively, by a pod conveying system.

As illustrated in an imaginary line in Fig. 8 and as a solid line in Fig. 9, the first pod 26A provided to the first pod stage 24 is pushed by a pod transfer device (not shown) toward the first wafer loading/unloading opening 20A to be in pressurized contact therewith. Then, a door 27 of the first pod 26A is separated by a pod door removing/restoring device 23A prepared at the first pod opener 21A so that the opening of the first pod 26A is opened and the wafer contained therein can be taken out.

A plurality of side dummy wafers (Wa) contained in the



first pod 26A are sequentially carried out of the first pod 26A and charged into a boat 9 by a wafer transfer device 10. At this time, the side dummy wafers (Wa) are properly distributed to be loaded in an upper end part and a lower end part of the boat 9, as shown in Fig. 9.

As shown in the imaginary line in Fig. 8, the second pod 26B provided to the second pod stage 25 is transferred by the pod transfer device (not shown) toward the second wafer loading/unloading opening 20B to be in pressurized contact therewith. Then, a door 27 of the second pod 26B is separated by a pod door removing/restoring device 23B prepared at the second pod opener 21B, so that the opening of the second pod 26B is opened and the wafers contained therein can be taken out.

A plurality of product wafers (Wb) contained in the second pod 26B are loaded into the boat 9 in sequence by the wafer transfer device 10. Herein, the product wafers (Wb) are loaded into the boat 9 in a manner that the product wafers (Wb) are distributed between two groups of the side dummy wafers (Wa) arranged at the upper end part and the lower end part of the boat 9, as shown in Fig. 9.

When the predetermined total number of, e.g., 30 to 32, side dummy wafers (Wa) and product wafers (Wb) are all loaded into the boat 9, the boat 9 is lifted by a boat elevator 7 and introduced into a process room within a process tube 4. If the boat 9 reaches an upper limit point,

a periphery of an upper surface of a sealing cap 8, which supports the boat 9, airtightly closes the process tube 4, so that the process room is hermetically sealed.

Then, the process room of the process tube 4 is  
5 evacuated through an exhaust pipe 6 to reduce the pressure therein down to a predetermined level. Thereafter, the process room is heated by a heater unit 3 to reach to a predetermined temperature. Next, a predetermined flow rate  
10 of process gas is supplied into the process room through a gas supply pipe 5. Through such operations, a desired film corresponding to pre-established process conditions is formed on the product wafers (Wb). The number of the product wafers that can be processed during one batch process is set to be equal to or less than the maximum  
15 number of product wafers that can be accommodated by one product substrate carrier, i.e., the second pod 26B. Further, all of the product wafers contained in one product substrate carrier, i.e., the second pod 26B, is set to be processed in the process room at the same time as in the  
20 first preferred embodiment of the present invention.

After a predetermined processing time has elapsed, the boat 9 is lowered down by the boat elevator 7 and returned to a waiting position with the processed product wafers (Wb) and the side dummy wafers (Wa) kept therein.

25 When the boat 9 is returned to the waiting position, the processed product wafers (Wb) are first discharged from

the boat 9 by the wafer transfer device 10 and carried into the empty second pod 26B that has been waiting on the second pod opener 21B.

5 After all the processed product wafers (Wb) are carried into the empty second pod 26B, the second pod 26B filled with the processed product wafers (Wb) are closed by the pod door 27 which is remounted thereon by the pod door removing/restoring device 23B. Then, as can be seen from the solid lines in Figs. 7 and 8, the second pod 26B is  
10 transferred from the second wafer loading/unloading opening 20B to the second pod stage 25 by the pod transfer device prepared at the second pod opener 21B.

Next, the used side dummy wafers (Wa) are carried from the boat 9 to the empty first pod 26A waiting on the first  
15 pod opener 21A by the wafer transfer device 10.

When all the used side dummy wafers (Wa) are completely carried into the first pod 26A, the door 27 of the first pod 26A is closed by the pod door removing/restoring device 23A and the first pod 26A is  
20 returned from the first wafer loading/unloading opening 20A to the first pod stage 24 by the pod transfer device of the first pod opener 21A.

Meanwhile, the second pod 26B returned to the second pod stage 25 along with the processed product wafers (Wb)  
25 contained therein is then transferred by the pod conveying system to a next process line for cleaning, a film test, or

the like. The processed product wafers (Wb) having films formed thereon are transferred to a lithography process line or an etching process line to be subjected to required treatments. As a result, IC devices can be finally produced in accordance with an IC manufacturing process of the present invention. Meanwhile, the first pod 26A returned to the first pod stage 24 while carrying therein the used side dummy wafers (Wa) is kept thereon to be repeatedly used for next batch processes as long as the side dummy wafers (Wa) can be reused. However, when degrees of bending or degrees of contamination of the side dummy wafers (Wa) become to exceed a tolerance limit after repeated uses, the first pod 26A containing therein the side dummy wafers (Wa) is transferred by the conveying system from the first pod stage 24 to, e.g., a side dummy wafer exchanging process line, which is one of the process lines for manufacturing the IC devices in accordance with the present invention.

Afterward, the above-described wafer processing method in accordance with the second embodiment are repeated to perform a batch process for everyone of, e.g., 25 product wafers by using the small-batch type CVD system. As a result, IC devices can be produced in accordance with the IC manufacturing process of the present invention.

In accordance with the second embodiment of the present invention, the tact time can be further shortened since the pod transfer device is not used.

The present invention is not limited to the above-described preferred embodiments but can be varied in various ways if required.

For example, the side dummy wafers (Wa) are carried by the pod 26 in the above-stated preferred embodiments. However, the side dummy wafers (Wa) can be accommodated by a wafer stocker 18, a storage of the side dummy wafers. In such case, the side dummy wafers (wa) are transferred between the boat 9 and the wafer stocker 18. Accordingly, it is not required to transfer the side dummy wafers (Wa) to a pod outside of the housing 2 for every stage, so that the tact time can be further diminished. The side dummy wafers (Wa) are repeatedly used until they are exchanged because their degree of bending or degree of contamination exceeds a tolerance limit.

Further, in the preferred embodiments, one dummy wafer pod and one product wafer pod are used and thus 25 sheets or less of product wafers contained in the product wafer pod can be processed during one batch process. However, by using the wafer stocker 18 for storing the dummy wafers thereon, two product wafer pods can be employed to provide product wafers, so that a maximum 50 sheets of product wafers can be processed during one batch process.

Still further, though the present invention has been illustrated with reference to a CVD system used for forming a CVD film on wafers in the preferred embodiments, the

present invention can also be applied to a variety of substrate processing systems including a film-formation system for forming oxide films, insulation films or metal films on wafers and a thermal processing equipment for forming oxide films on wafers, diffusing impurities on wafers or reflowing/annealing for an activation or a planarization of carriers after ion injection.

In addition, though the present invention has been used in manufacturing IC devices in the preferred embodiments, the present invention can also be utilized to produce all kinds of semiconductor devices such as a transistor, a diode, a capacitor, an optical semiconductor device, a hybride IC device, and the like.

Further, substrates to be processed can be photo masks, printed circuit boards, liquid crystal panels, and so forth other than semiconductor wafers.

As described above, the present invention can provide a QTAT (quick turn around time) type substrate processing apparatus capable of processing substrates in small amounts at a reduced cost.

While the present invention has been shown and described with respect to the preferred embodiments, it will be understood by those skilled in the art that various changes and modifications may be made without departing from the spirit and scope of the invention as defined in the following claims.